

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	5	"714"/\$.ccls. and CPU NEAR memory with test\$3 NEAR program	USPAT	OR	OFF	2004/02/23 10:43
S5	6	"714"/\$.ccls. and CPU NEAR memory NEAR test\$3	USPAT	OR	OFF	2004/02/23 10:59
S6	12	714/736.ccls. and CPU NEAR memory	USPAT	OR	OFF	2004/02/23 11:02
S7	5	714/736.ccls. and CPU NEAR memory and (defect\$3 or fault) with compar\$4	USPAT	OR	OFF	2004/02/23 11:05
S8	23	INTEL.as. and CPU NEAR memory and (defect\$3 or fault)	USPAT	OR	OFF	2004/02/23 11:06
S9	1	INTEL.as. and CPU NEAR memory and fault near3 detection	USPAT	OR	OFF	2004/02/23 11:07
S10	54	INTEL.as. and fault near3 detection	USPAT	OR	OFF	2004/02/23 11:08
S11	14	INTEL.as. and memory with fault near3 detection	USPAT	OR	OFF	2004/02/23 11:09
S12	7	INTEL.as. and memory with fault near3 detection and computer adj system	USPAT	OR	OFF	2004/02/23 11:12
S15	15	("5388224" "5513327" "5598528" "5634137" "5655148" "5692189" "5748980" "5751936" "5787246" "5793979" "5809329" "5974546" "6023271" "6044411" "6044423").PN.	USPAT	OR	OFF	2004/02/23 11:28
S14	77	"714"/\$.ccls. and CPU near3 memory with test\$3 and computer adj system	USPAT	OR	OFF	2004/02/23 12:05
S18	12	714/736.ccls. and CPU near3 memory with test\$3	USPAT	OR	OFF	2004/02/23 12:21
S19	5	714/736.ccls. and CPU near3 memory with test\$3 and computer adj system	USPAT	OR	OFF	2004/02/23 12:23
S20	1	714/736.ccls. and CPU near3 memory near test\$3 and computer adj system	USPAT	OR	OFF	2004/02/23 12:24
S17	203	"714"/\$.ccls. and CPU near3 memory with test\$3	USPAT	OR	OFF	2004/02/23 13:02
S21	8	"714"/\$.ccls. and CPU near3 memory with test\$3 with (defect or faulure or fault)	USPAT	OR	OFF	2004/02/23 13:06

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S22	11	"714"/\$.ccls. and CPU near3 memory with test\$3 with (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:35
S23	11	"714"/\$.ccls. and CPU near3 memory with test\$3 with (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:37
S25	35	"714"/\$.ccls. and CPU near3 memory with test\$3 and test adj program and (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:40
S24	5	"714"/\$.ccls. and CPU near3 memory with test\$3 and test adj program with (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:40
S26	51	"714"/\$.ccls. and CPU near3 memory with test\$3 and test adj pattern and (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:42
S27	5	"714"/\$.ccls. and CPU near3 main adj memory with (defect\$2 or faulure or fault)	USPAT	OR	ON	2004/02/23 13:43
S13	14	"714"/\$.ccls. and CPU near3 memory with fault near3 detection and computer adj system	USPAT	OR	OFF	2004/02/23 13:46
S29	30801	"714"/\$.ccls. and CPU near memory with testing woth (fault or failure or defect\$3) and interrupt	USPAT	OR	OFF	2004/02/23 14:03
S33	15	"714"/\$.ccls. and CPU near memory near (fault or failure or defect\$3)	USPAT	OR	OFF	2004/02/23 14:06
S32	1	"714"/\$.ccls. and CPU near memory with test\$3 with (fault or failure or defect\$3)	USPAT	OR	OFF	2004/02/23 14:06
S1	751	"714"/\$.ccls. and CPU NEAR memory	USPAT	OR	OFF	2004/02/23 14:08
S2	6	"714"/\$.ccls. and CPU NEAR memory NEAR test\$3	USPAT	OR	OFF	2004/02/23 14:10
S35	1	"714"/\$.ccls. and CPU NEAR memory with BIST	USPAT	OR	ON	2004/02/23 14:11
S36	11	"714"/\$.ccls. and CPU near memory with (fault or failure or defect\$3) same computer adj system	USPAT	OR	OFF	2004/02/23 14:12
S37	19	"714"/\$.ccls. and CPU near memory with (fault or failure or defect\$3) same computer adj system	USPAT	OR	ON	2004/02/23 14:14

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S38	16	"714"/\$.ccls. and CPU near memory with (fault or failure or defect\$3) same computer adj system and compar\$4	USPAT	OR	ON	2004/02/23 14:56
S39	1	"714"/\$.ccls. and CPU near memory with test\$3 with (fault or failure or defect\$3)	USPAT	OR	ON	2004/02/23 15:00
S40	86	"714"/\$.ccls. and CPU near memory with test\$3	USPAT	OR	ON	2004/02/23 15:02
S41	23	"714"/\$.ccls. and CPU near memory near3 test\$3	USPAT	OR	ON	2004/02/23 15:07
S42	2	"714"/\$.ccls. and CPU near memory near3 test\$3 and simulation	USPAT	OR	ON	2004/02/23 15:08
S45	1	"714"/\$.ccls. and CPU near memory same test adj pattern with simulation	USPAT	OR	ON	2004/02/23 15:11
S47	605	714/736.ccls.	USPAT	OR	ON	2004/02/23 15:13
S46	2	"714"/\$.ccls. and CPU near memory with test adj pattern	USPAT	OR	ON	2004/02/23 15:13
S48	98	714/736.ccls. and CPU and memory	USPAT	OR	ON	2004/02/23 15:17
S49	33	714/736.ccls. and CPU and memory with test\$3 with compar\$4	USPAT	OR	ON	2004/02/23 15:20
S51	5	714/736.ccls. and CPU near memory with test\$3	USPAT	OR	ON	2004/02/23 15:28
S50	5	714/736.ccls. and CPU with memory with test\$3 with compar\$4	USPAT	OR	ON	2004/02/23 15:28
S43	14	"714"/\$.ccls. and CPU near memory and test adj pattern with simulation	USPAT	OR	ON	2004/02/23 15:29
S52	25	("4654852" "4817092" "4964130" "5386551" "5537596" "5596716" "5598528" "5634137" "5655148" "5692189" "5740354" "5748980" "5751936" "5787246" "5793979" "5809329" "5974546" "6000040" "6032271" "6044423" "6122756" "6128555" "6226700" "6243838" "6282459").PN.	USPAT	OR	OFF	2004/02/23 15:31
S53	134	"714"/\$.ccls. and CPU near memory and (fault or failure or defect\$3) with computer adj system	USPAT	OR	OFF	2004/02/23 15:33
S28	42	"714"/\$.ccls. and CPU near memory with (fault or failure or defect\$3) and computer adj system	USPAT	OR	OFF	2004/02/23 15:33

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S55	12	"714"/\$.ccls. and CPU near memory with test\$3 and (fault or failure or defect\$3) with computer adj system	USPAT	OR	OFF	2004/02/23 18:30
S58	2	"714"/\$.ccls. and CPU near (tim\$3 or clock) with test\$3 and (fault or failure or defect\$3) with computer adj system	USPAT	OR	OFF	2004/02/23 18:32
S60	765	"714"/\$.ccls. and CPU with (execution near tim\$3 or clock)	USPAT	OR	OFF	2004/02/23 18:33
S59	1	"714"/\$.ccls. and CPU with (execution near tim\$3 or clock) with test\$3 and (fault or failure or defect\$3) with computer adj system	USPAT	OR	OFF	2004/02/23 18:33
S61	8	"714"/\$.ccls. and CPU adj execution adj time	USPAT	OR	OFF	2004/02/23 18:35
S63	50	"714"/\$.ccls. and CPU adj clock with time	USPAT	OR	ON	2004/02/23 18:36
S62	151	"714"/\$.ccls. and CPU adj clock	USPAT	OR	OFF	2004/02/23 18:36
S64	6	"714"/\$.ccls. and CPU adj clock NEAR time	USPAT	OR	ON	2004/02/23 18:37
S66	4	"714"/\$.ccls. and CPU adj clock with measur\$3	USPAT	OR	ON	2004/02/23 18:39
S67	7	"714"/\$.ccls. and CPU adj clock same measur\$3	USPAT	OR	ON	2004/02/23 18:40
S68	10	"714"/\$.ccls. and CPU with run with time same measur\$3	USPAT	OR	ON	2004/02/24 09:48
S16	1	"4654852".PN.	USPAT	OR	OFF	2006/03/15 17:13
S70	34	CPU NEAR3 instruction with memory with test\$3	USPAT	OR	OFF	2006/03/15 17:22
S4	78	"714"/\$.ccls. and CPU NEAR memory with test\$3	USPAT	OR	OFF	2006/03/15 17:22
S72	7	CPU NEAR3 instruction with memory with test adj (program copde pattern)	USPAT	OR	ON	2006/03/15 17:30
S73	4	CPU NEAR3 instruction with memory with test adj (program copde pattern) and (error dfect\$3 fail\$3)	USPAT	OR	ON	2006/03/15 17:33
S76	10	CPU NEAR3 instruction near3 memory and test adj (program copde pattern) with (error dfect\$3 fail\$3)	USPAT	OR	ON	2006/03/15 17:34
S75	73	CPU NEAR3 instruction near3 memory and test adj (program copde pattern) and (error dfect\$3 fail\$3)	USPAT	OR	ON	2006/03/15 17:34

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S74	328	CPU NEAR3 instruction and memory and test adj (program copde pattern) and (error dfect\$3 fail\$3)	USPAT	OR	ON	2006/03/15 17:34
S77	7	("4862347" "4924429" "4926322" "4937770" "5105374" "5193068" "5265233").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/15 17:37
S81	2	S77 and CPU and memory	USPAT	OR	ON	2006/03/15 17:39
S82	1	("6144930").URPN.	USPAT	OR	OFF	2006/03/15 17:40
S71	69	CPU NEAR3 instruction with memory with test\$3	USPAT	OR	ON	2006/03/15 17:42
S83	35	CPU NEAR3 instruction with memory near3 test\$3	USPAT	OR	ON	2006/03/15 17:49
S84	20	CPU NEAR3 instruction with memory near3 test\$3 and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:51
S85	54864	CPU NEAR3 instruction and memory near3 test\$3 near3 compar\$5 near3 expected nera3 error and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:53
S86	1	CPU NEAR3 instruction and memory with test\$3 with compar\$5 with expected with (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:54
S87	1	CPU NEAR3 instruction and memory with compar\$5 with expected with (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:55
S88	11	CPU NEAR3 instruction with memory and compar\$5 with expected with (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:57
S91	1	CPU NEAR3 instruction with memory near test\$3 and (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/03/15 17:59
S92	18	CPU NEAR3 instruction with memory near3 test\$3 and (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/03/15 18:01
S93	29	CPU NEAR3 instruction with memory near3 test\$3 and (error fail\$3)	USPAT	OR	ON	2006/03/15 18:03
S96	3	714/710-711.ccls. and CPU NEAR3 memory near test\$3	USPAT	OR	ON	2006/03/15 18:06
S95	15	714/710-711.ccls. and CPU NEAR3 instruction with memory	USPAT	OR	ON	2006/03/16 11:36
S97	1	714/710-711.ccls. and CPU NEAR3 instruction with memory and redundant and replacement	USPAT	OR	ON	2006/03/16 11:37

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S98	27	714/710-711.ccls. and CPU with memory and redundant and replacement	USPAT	OR	ON	2006/03/16 11:39
S99	23	714/710-711.ccls. and CPU with memory and redundant and replacement and @ad<"20000306"	USPAT	OR	ON	2006/03/16 11:40
S100	3006622	714/710-711.ccls. and CPU with memory and redundant and replacement and allocat\$3 and dealloact\$3and @ad<"20000306"	USPAT	OR	ON	2006/03/16 11:41
S102	10	714/710-711.ccls. and CPU with memory and redundant and replacement and allocat\$3 and @ad<"20000306"	USPAT	OR	ON	2006/03/16 11:43
S104	3	714/710-711.ccls. and CPU with memory and (redundant spare replacement) with allocat\$3 and @ad<"20000306"	USPAT	OR	ON	2006/03/16 11:45
S106	6	714/710-711.ccls. and CPU and memory with (deallocat\$3 remov\$3) and (redundant spare replacement) and @ad<"20000306"	USPAT	OR	ON	2006/03/16 11:49
S108	2	714/710-711.ccls. and CPU and memory with (deallocat\$3 alloact\$3 remov\$3) with defect\$3 and @ad<"20000306"	USPAT	OR	ON	2006/03/16 12:07
S107	2	714/710-711.ccls. and CPU and memory with (deallocat\$3 remov\$3) with defect\$3 and (redundant spare replacement) and @ad<"20000306"	USPAT	OR	ON	2006/03/16 12:07
S109	5	"6040574"	USPAT	OR	ON	2006/03/16 13:39
S111	27	714/710-711.ccls. and CPU with memory and redundant and replacement	USPAT	OR	ON	2006/08/23 14:44
S112	30	CPU NEAR3 instruction with memory near3 test\$3 and (error fail\$3)	USPAT	OR	ON	2006/08/23 14:45
S113	11	CPU NEAR3 instruction with memory and compar\$5 with expected with (error fail\$3) and comput\$3 adj system	USPAT	OR	ON	2006/08/23 14:46
S110	73	INTEL.as. and fault near3 detection	USPAT	OR	OFF	2006/08/23 14:46
S115	9	INTEL.as. and (fault near3 detection).CLM.	USPAT	OR	OFF	2006/08/23 14:47

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S11 4	73	INTEL.as. and (fault near3 detection)".CLM"	USPAT	OR	OFF	2006/08/23 14:47
S11 7	8	INTEL.as. and (DEFECT near3 detect\$3).CLM.	USPAT	OR	OFF	2006/08/23 14:48
S11 6	3	INTEL.as. and (DEFECT near3 detection).CLM.	USPAT	OR	OFF	2006/08/23 14:48
S12 4	13	KORHONEN adj AKI.in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/08/23 14:51
S12 3	9	KORHONEN adj AKI.in.	US-PGPUB; USPAT	OR	ON	2006/08/23 14:51
S12 5	2	KORHONEN adj AKI.in. and (memory).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/08/23 14:52
S13 1	319	INTEL.as. and (system adj memory).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/08/23 14:54
S13 5	32	INTEL.as. and (failure with memory).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/08/23 14:55
S13 4	3	INTEL.as. and (failure with system adj memory).CLM.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/08/23 14:55